

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)

Choice Based Credit System (CBCS) Scheme

Bachelor of Technology (Electronics & Communication)

IV - SEMESTER

| Sr. No. | Subject Code | Name of Subject | Course Code | Teaching Scheme/ Week | | | Examination Scheme | | | | | Total Marks | Credits |
|--------------|--------------|---|-------------|-----------------------|----------|-----------|-------------------------|---------------|----------------------|-------------------------|----------------------|-------------|-----------|
| | | | | L | T | P | Theory | | | Practical | | | |
| | | | | | | | End Sem University Exam | Two Term Exam | Teachers Assessment* | End Sem University Exam | Teachers Assessment* | | |
| 1 | BTEC401 | Linear Integrated Circuits | | 3 | 1 | 2 | 60 | 20 | 20 | 30 | 20 | 150 | 5 |
| 2 | BTEC402 | Digital Electronics | | 3 | 1 | 2 | 60 | 20 | 20 | 30 | 20 | 150 | 5 |
| 3 | BTEC403 | Electro Magnetic Theory | | 3 | 1 | 0 | 60 | 20 | 20 | 0 | 0 | 100 | 4 |
| 4 | BTEC404 | Analog Communication | | 3 | 1 | 2 | 60 | 20 | 20 | 30 | 20 | 150 | 5 |
| 5 | ML301 | Environmental & Energy Studies | | 3 | 1 | 0 | 60 | 20 | 20 | 0 | 0 | 100 | 4 |
| 6 | BTEC405 | Personality Development-2 (Quantitative Aptitude & reasoning + Technical Writing) | | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 50 | 50 | 1 |
| 7 | BTCS207 | Computer Programming-II | | 0 | 0 | 2 | 0 | 0 | 0 | 30 | 20 | 50 | 1 |
| 8 | BTEC406 | Micro Project | | 0 | 0 | 2 | 0 | 0 | 0 | 30 | 20 | 50 | 1 |
| TOTAL | | | | 15 | 5 | 12 | 300 | 100 | 100 | 150 | 130 | 800 | 26 |

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.



**Chairperson
Board of Studies**

Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore



Joint Registrar

Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore